## **DECLARATION FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled POWER CONVERTER METHOD AND APPARATUS HAVING HIGH INPUT POWER FACTOR AND LOW HARMONIC DISTORTION, the specification of which:

[x]	is attached hereto.	
[]	was filed on	as
	Application Serial No.	

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Sec. 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Sec. 119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed: None

I hereby claim the benefit under Title 35, United States Code, Sec. 119(e) of any United States provisional application listed below:

Provisional Application No.

Filing Date

60/420,193

October 21, 2002

I hereby claim the benefit under Title 35, United States Code, Sec. 120 of any United States application(s), or Sec. 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, Sec. 112. I acknowledge the duty to disclose information which is material to patentability as defined in

Title 37, Code of Federal Regulations, Sec. 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application: none

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

validity of the applica	or any paten	it issued thereon.	
Full name of first co-inventor:		Vivek Agarwal	
Inventor's signature:			(Date)
Residence:	India		(=,
Citizenship:	India		
Post Office address:	Indian Instit	of Electrical Engineering ute of Technology - Bombay nbai - 400 076	
Full name of second	co-inventor:	Victor Prince Sundarsin	ngh
Inventor's signature:	<u></u>		(Date)
Residence:	India		(=,
Citizenship:	India		
Post Office address:	Indian Instit	of Electrical Engineering ute of Technology - Bombay abai - 400 076	

Full name of third co-inventor: Serge Bontemps						
Inventor's signature:		(Date)				
Residence:	France					
Citizenship:	India					
Post Office address:	Residence Pontet-Noailles Batiment B12 Rue Paul Eluard, 33600 Pessac France					
Full name of fourth co-inventor: Alain Calmels						
Inventor's signature:		(Date)				
Residence:	France					
Citizenship:	France					
Post Office address:	Residence Le Bocage, Appartement #9 Allee des Foreurs, 33400 Telence France					